# **Electronic Acknowledgement Receipt** FES ID: 1148896 Application Number: 10710699 Confirmation Number: 4698 ACCURATE TIMING ANALYSIS OF INTEGRATED CIRCUITS WHEN Title of Invention: COMBINATORIAL LOGIC OFFERS A LOAD First Named Inventor: Somasekar JAYARAMAN Customer Number: 23494 Filer: William Brian Kempler/Shannon Trotter Filer Authorized By: William Brian Kempler Attorney Docket Number: TI-37107 Receipt Date: 10-AUG-2006 Filing Date: 29-JUL-2004 Time Stamp: 11:48:56 Application Type: Utility International Application Number:

## Payment information:

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### File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part	Pages		
1		37107_Amnd_111.pdf	26397	yes	8		
	Multipart Description						
	Doc Desc		Start	End			
	Amendment - After Non-Final Rejection		1	1			
	Specification		2	2			
	Claims		3	5			
	Applicant Arguments/Remarks Made in an Amendment		6	8			
Warnings:			•				
Information:							
2	Fee Worksheet (PTO-875)	fee-info.pdf	8202	no	2		
Warnings:							
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	Total Files Size (in bytes): 34599						

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#### New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

#### National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.